O P E 4035

## PATENT APPLICATION

## THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

John Bruno

Examiner: Nathan W. Ha

Serial No.:

10/064,856

Art Unit: 2814

Filing Date:

August 23, 2002

Docket No.: 00100.02.0038

Title: INTEGRATED CIRCUIT HAVING MEMORY DISPOSED THEREON AND METHOD OF MAKING THEREOF

Mail Stop RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Certificate of First Class Mailing

I hereby certify that this paper is being sent via first class mail, postage prepaid, in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P. O. Box 1450,

Alexandria, VA 22313-1450, on this date:

1-19-06

Christine Wr

PRELIMINARY AMENDMENT

Dear Sir:

In response to the Final Office Action mailed July 19, 2005 and the Advisory Action mailed December 20, 2005, Applicant submits a Request for Continued Examination and the following preliminary amendment and response:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.